

4. The regulator of claim 1 wherein said controller is further configured to adjust said gain factor as required to produce said regulated input voltages according to said loadline, and wherein said loadline specifies a total power voltage-current relationship.

5. The regulator of claim 1 wherein said controller is further configured to adjust said gain factor as required to produce said regulated input voltages according to said loadline, and wherein said loadline specifies a non-linear relationship for said plurality of input voltage required values.

6. The regulator of claim 5 wherein said non-linear loadline further includes a discontinuity corresponding to an intermediate current value between zero and maximum, associated with said microelectronics device.

7. A regulator, comprising:

a controller including a comparator and a threshold detector, wherein an input of said comparator is coupled to the output of said threshold detector;

a switch coupled to said controller and operable in response to a signal provided by said controller, said switch being connected to an inductor, a diode, and an output capacitor arranged in a network that produces a load current in response to an input source voltage received via said switch, said network having an output coupled to a microelectronics device for supplying a plurality of regulated input voltages to said microelectronics device; and

a current sense feedback network connected to said network output and having a gain factor, said feedback network coupled to said threshold detector to cause said threshold detector to produce an output signal as a product of said gain factor;

wherein said controller is configured to produce said regulated input voltages by varying the duty cycle of said switch in accordance with a voltage-current loadline;

said controller being further configured to maintain said regulated input voltages within an input voltage range bounded by a constant upper limit and a lower limit;

Sub.
Q4

REGISTRATION

22. The electronic system of claim 19 wherein said regulator is further configured to determine said gain factor in order to produce said regulated input voltages according to a loadline, wherein said loadline specifies a linear relationship for said plurality of said regulated input voltage required values, and wherein said lower limit is equal to the lesser of the products of a tolerance level multiplied by each said input voltage required value.

23. The electronic system of claim 19 wherein said regulator adjusts said gain factor as required to produce said regulated input voltages according to a loadline, and wherein said loadline specifies a total power voltage-current relationship.

24. The electronic system of claim 19 wherein said regulator adjusts said gain factor as required to produce said regulated input voltages according to a loadline, and wherein said loadline specifies a non-linear relationship for said plurality of input voltage required values.

25. The electronic system of claim 24 wherein said non-linear loadline further includes a discontinuity corresponding to an intermediate current value between zero and maximum, associated with said microelectronics device.

26. A regulating method, comprising:

supplying multiple input voltages to one or more microelectronics devices, said multiple input voltages including a plurality of input voltage required values;

determining a lower limit of a voltage regulation range for said multiple input voltages in accordance with a voltage-current loadline; and

maintaining each of said input voltages supplied to said microelectronics devices within said range.

Sub.
C-7

2025 RELEASE UNDER E.O. 14176

27. The method of claim 26 wherein the number of said regulated input voltages is two.

Pub. A8

28. The method of claim 26 wherein said determining further includes selecting a gain factor in order to produce said regulated input voltages according to said loadline, wherein said loadline specifies a linear relationship for said plurality of said regulated input voltage required values wherein said lower limit is equal to the lesser of the products of a tolerance level multiplied by each said input voltage required value.

29. The method of claim 26 wherein said determining further includes adjusting a gain factor as required to produce said regulated input voltages according to said loadline, wherein said loadline is a total power voltage-current loadline.

30. The method of claim 26 wherein said determining further includes adjusting a gain factor as required to produce said regulated input voltages according to said loadline, wherein said loadline specifies a non-linear relationship for said plurality of said regulated input voltage required values.

*add }
A9*